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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO.	TOT CLAIMS	IND CLAIMS
10/557,104	11/27/2006	2816	1030	20031035-02	12	3

CONFIRMATION NO. 5365

## FILING RECEIPT

27623

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STAMFORD, CT06901

Date Mailed: 03/28/2007

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## Applicant(s)

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Power of Attorney: The patent practitioners associated with Customer Number 27623

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/EP03/50307 07/15/2003

## Foreign Applications

If Required, Foreign Filing License Granted: 03/27/2007

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is  
**US10/557,104**

Projected Publication Date: 07/05/2007

Non-Publication Request: No

Early Publication Request: No

## Title

Integrated circuit with bit error test capability

## Preliminary Class

327

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